

15750 U.S. PTO
112403

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT
W&B Docket No.: INF 2008-US/PC
OC Docket No.: INFN/0037

MAIL STOP PATENT APPLICATION
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE UNDER 37 CFR 1.10

I hereby certify that this correspondence and the documents referred to as attached therein are being deposited on November 24, 2003 with the United States Postal Service in an envelope as "Express Mail Post Office to Addressee," mailing label No. EV335472255US addressed to: Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

November 24, 2003

Date

Signature

Re: Inventor(s): TILL SCHLÖSSER and BRIAN LEE

Title: DRAM CELL ARRANGEMENT WITH VERTICAL MOS TRANSISTORS, AND METHOD FOR ITS FABRICATION

Transmitted herewith is the patent application identified above, including:

- ☒ Specification, claims and abstract 16 Total Pages
☒ Drawings ☐ Formal ☒ Informal 3 Total Pages
☒ Declaration and Power of Attorney (Unsigned)
☒ Information Disclosure Statement with List
☐ Assignment of the Invention to
☐ Assignment Recordation Cover Sheet

22154 U.S. PTO
10/720730

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FEE CALCULATION

	NUMBER OF CLAIMS FILED	LESS NUMBER PAID BY BASIC FEE	NUMBER OF EXTRA CLAIMS (Not less than zero)	ENTITY FEE
Basic Fee				\$.00
Total Claims	24	- 20 = 4	X \$18 =	\$.00
Independent Claims	4	- 3 = 2	X \$86 =	\$.00
First Presentation of Multiple Dependent Claims			+ \$.00	-0-
Total Filing Fee Calculation				\$.00

- ☐ The Commissioner is hereby authorized to charge \$ to Deposit Account No. **A duplicate copy of this transmittal is enclosed.**
- ☐ The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account No.. **A duplicate copy of this transmittal is enclosed.**
- ☒ Please address all future correspondence to:

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Respectfully submitted,

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